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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/602,503	02/20/1996	MICHAEL B. BALL	2718US	4539

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EXAMINER

NGUYEN, DILINH P

ART UNIT PAPER NUMBER

2814

DATE MAILED: 05/29/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

08/602,503

Applicant(s)

BALL, MICHAEL B.

Examiner

DiLinh Nguyen

Art Unit

2814

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 29 March 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 19,21-23 and 25-34 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 19,21-23 and 25-34 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on \_\_\_\_\_ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449) Paper No(s) 19.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). \_\_\_\_\_.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other:

## DETAILED ACTION

### ***Claim Rejections - 35 USC § 112***

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 19, 21-23 and 25-34 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

Regarding claim 19, the subject matter "...electrically conductive adhesive..." is not described in the specification. The specification disclose "...an electrically insulative adhesive..." page 6, line 20.

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

4. Claim 34 is rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The phrase: "...another discrete component to the substrate..." is not understood. Claim 34 is depending to claim 33 and the specification discloses only one discrete component to the substrate, regarding to fig. 5.

***Claim Rejections - 35 USC § 102***

5. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

6. Claim 19, in so far as it is understood, is rejected under 35 U.S.C. 102(b) as being anticipated by Yamauchi (JP. Pat. 5-13665).

Yamauchi discloses a method of fabricating a multi-die assembly (Fig. 1, abstract) comprising:

providing a substrate 1 including a plurality of conductors 3;

attaching at least one active face down base die 2a to the substrate in electrical communication with at least some of the plurality of conductors;

securing the back side of at least one active face up stack die 2a to the at least one base die with an adhesive 7;

electrically connecting at least one stack die to at least one of the conductors, and electrically grounding at least one base die via the electrically conductive adhesive and at least one stack die.

***Claim Rejections - 35 USC § 103***

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 21-23, 25-29 and 33-34, in so far as they are understood, are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamauchi in view of Fogal et al. (U.S. Pat. 5323060).

Yamauchi fails to disclose at least another stack die to the assembly and securing at least one discrete component to at least one of the stack die, the base die and the substrate.

- Regarding claims 23 and 25, Fogal et al. discloses a multi-chip semiconductor (fig. 1, column 2, lines 35 et seq.) comprising: securing at least another stack die 54 to the assembly; and securing the stack die 54 to at least one of the stack die 28 to provide a wire bonding of multichip modules to multichip module substrate.
- Regarding claims 21-22, 26-29 and 33-34, Fogal et al. disclose a multichip module (fig. 5, column 3, lines 43 et seq.) comprising :  
a discrete component 75 to the substrate 12;  
a discrete components 76 and 78 to an adhesive layer 77 to an upper uppermost chip 85; and

a bond wires 44a, 44b, and 79-81, wherein the bond wires bonding to the substrate and the chips. Fogal et al. show that discrete components can be added, while it is not specifically pointed out, the discrete component could include a filter (by pass) capacitor (column 3, line 53) which is needed for proper device operation and is not normally formed as part of a chip. It would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yamauchi to provide additional necessary components.

9. Claims 30-32, in so far as they are understood, are rejected under 35 U.S.C. 103(a) as being unpatentable over Yamauchi in view of Fogal et al. (U.S. Pat. 5323060) and further in view of Rostoker (U.S. Pat. 5399898).

Yamauchi and Fogal et al. disclose the claimed invention except for not further disclose the face down base die includes attaching at least two active face down base die to the substrate.

Rostoker discloses the attaching at least one active face down base die includes attaching at least two active face down base die 404 and 410 (fig. 4a, column 14, lines 40 et seq.) to the substrate 402 and electrically coupling each of the base die with one of the plurality substrate conductors 408 and 414; and

a bridging 416 at least one stack die between the two base die, the assembly is forming should that none of the dice are arranged face to face and limited overlap. Therefore, it would have been obvious to one having ordinary skill in the art at the time the invention was made to modify the device of Yamauchi and Fogal et al. to provide a greater power dissipation and a natural convection cooling channel, as shown by Rostoker.

### ***Response to Arguments***

Applicant's arguments filed 3/29/02 have been fully considered but they are not persuasive.

The applicant argues that Yamauchi fails to teach the adhesive utilized be an electrically conductive adhesive.

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The subject matter "...electrically conductive adhesive..." is not described in the specification. The specification disclose "...an electrically insulative adhesive..." page 6, line 20. In so far as it is understood, Yamauchi discloses the claimed invention.

**Conclusion**

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DiLinh Nguyen whose telephone number is (703) 305-6983. The examiner can normally be reached on 8:00AM - 6:00PM (M-F).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, OLIK CHAUDHURI can be reached on (703) 308-2794. The fax phone numbers for the organization where this application or proceeding is assigned are (703) 308-7722 for regular communications and (703) 308-7724 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-0956.

DLN  
May 22, 2002



Douglas Wille  
Patent Examiner